

Figure 1

FIG. 2 is a block diagram of a system 104, in accordance with an embodiment of the present invention. The system 104 includes a processor 202, a memory 206, a secondary storage 216, a network interface 218, and an input/output interface 220. The processor 202 is connected to the memory 206, the secondary storage 216, the network interface 218, and the input/output interface 220. The memory 206 includes an operating system 208, a registration process 210, a registrar database 212, and an audit file 214. The network interface 218 is connected to networks 112 and 116.

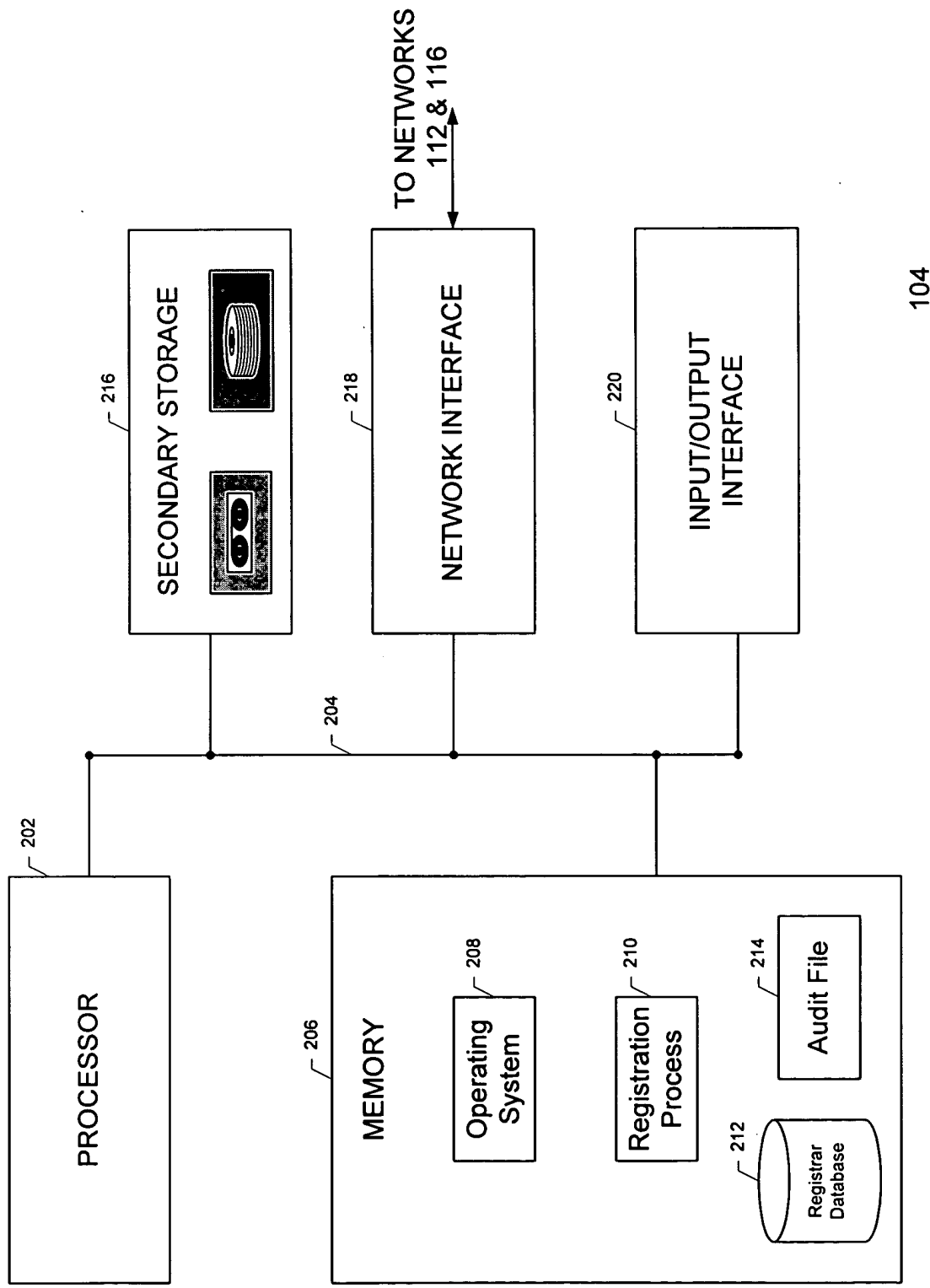


Figure 2

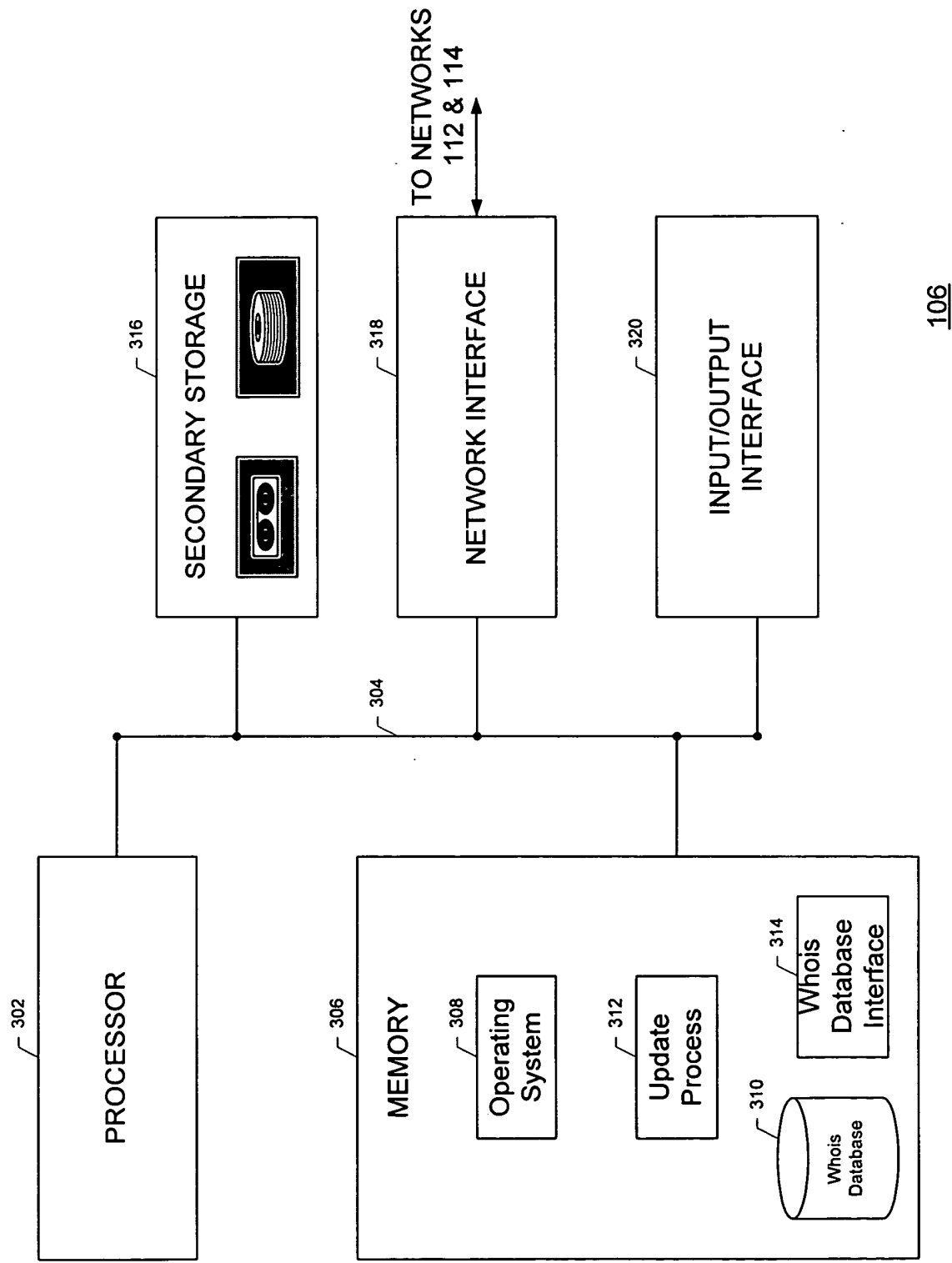


Figure 3

FIG. 4 is a block diagram of a computer system 400. The computer system 400 includes a processor 402, a memory 406, a secondary storage 412, a network interface 414, and an input/output interface 416. The processor 402 is connected to the memory 406, the secondary storage 412, the network interface 414, and the input/output interface 416. The memory 406 includes an operating system 408 and two Whois Service Processes 410. The network interface 414 is connected to networks 114 and 116.

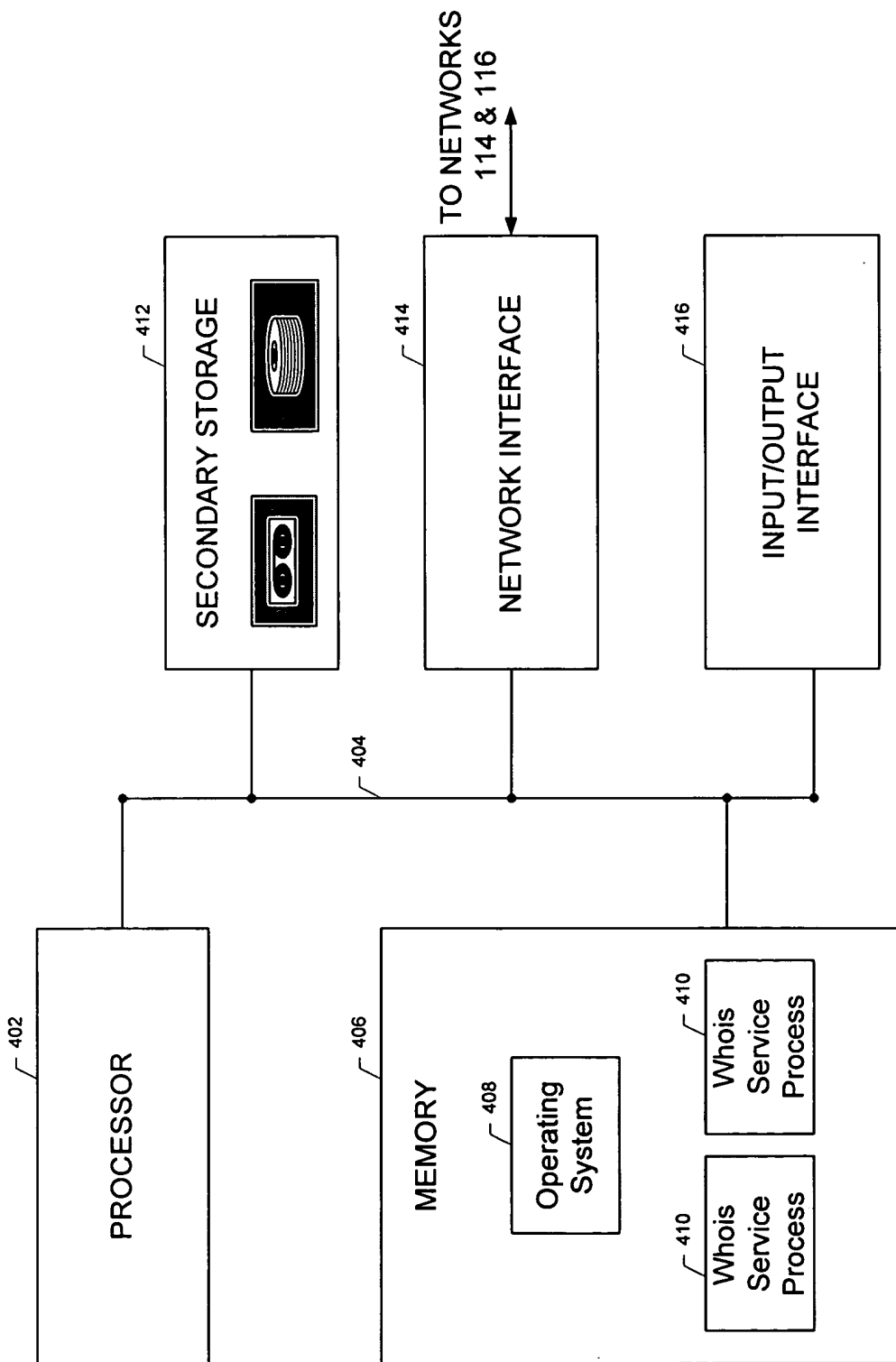


Figure 4

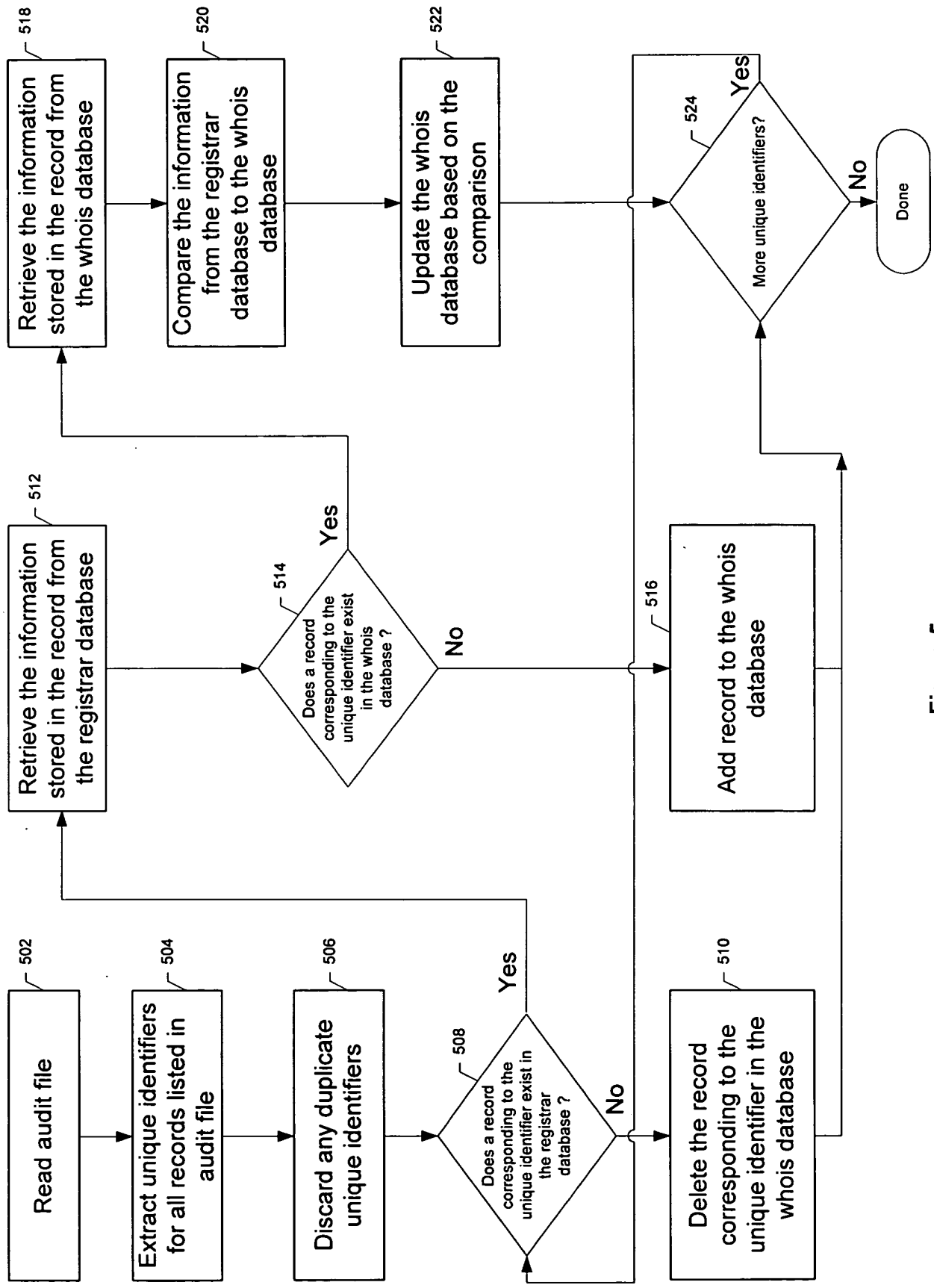


Figure 5

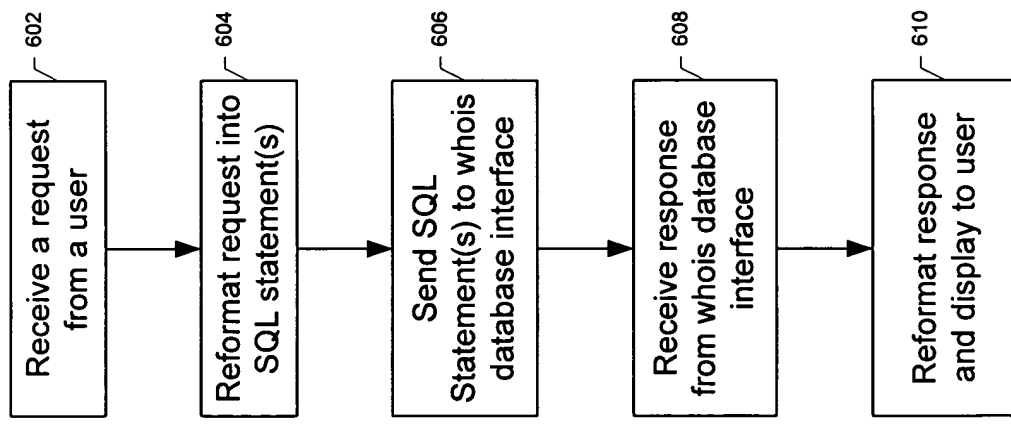


Figure 6

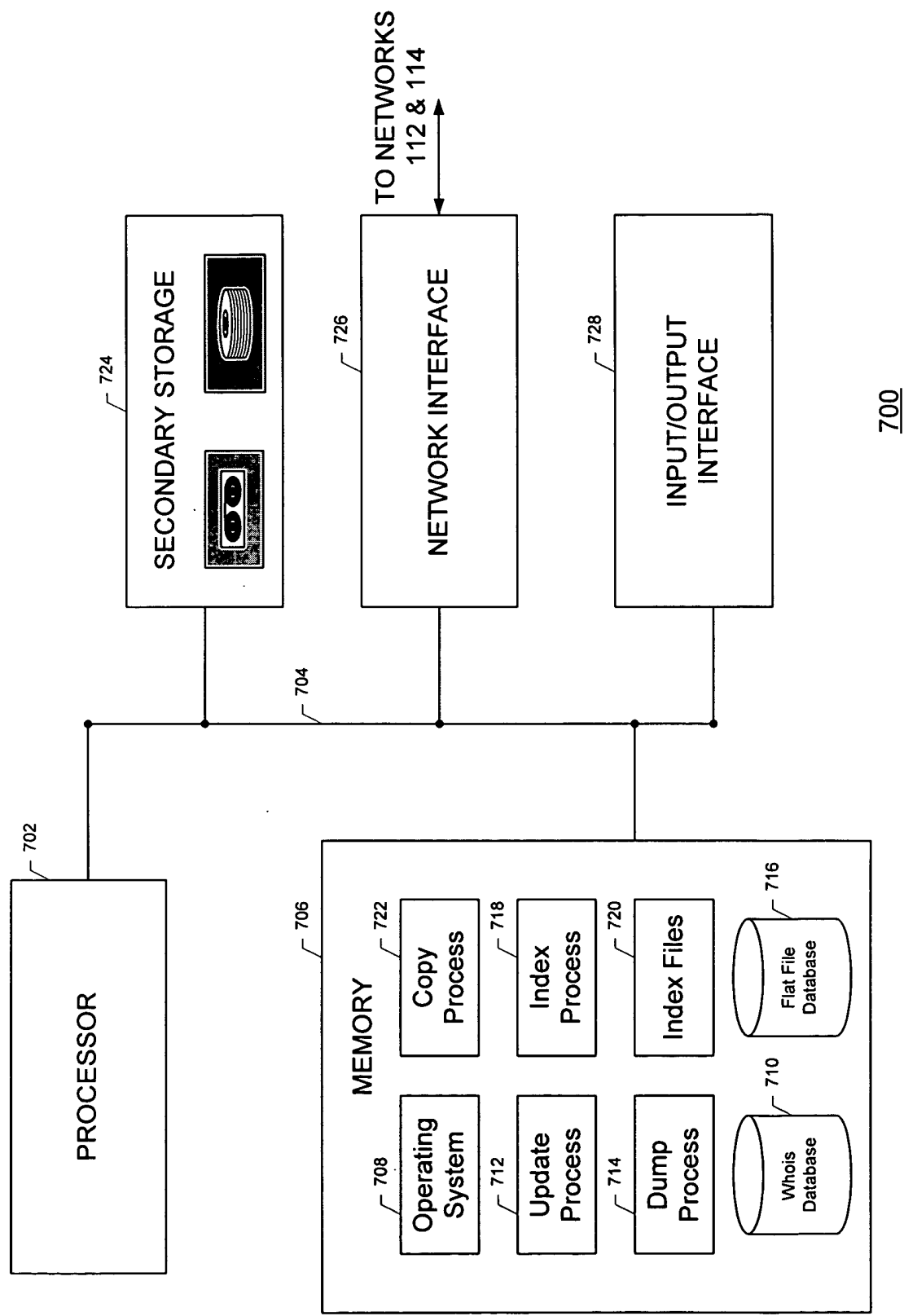
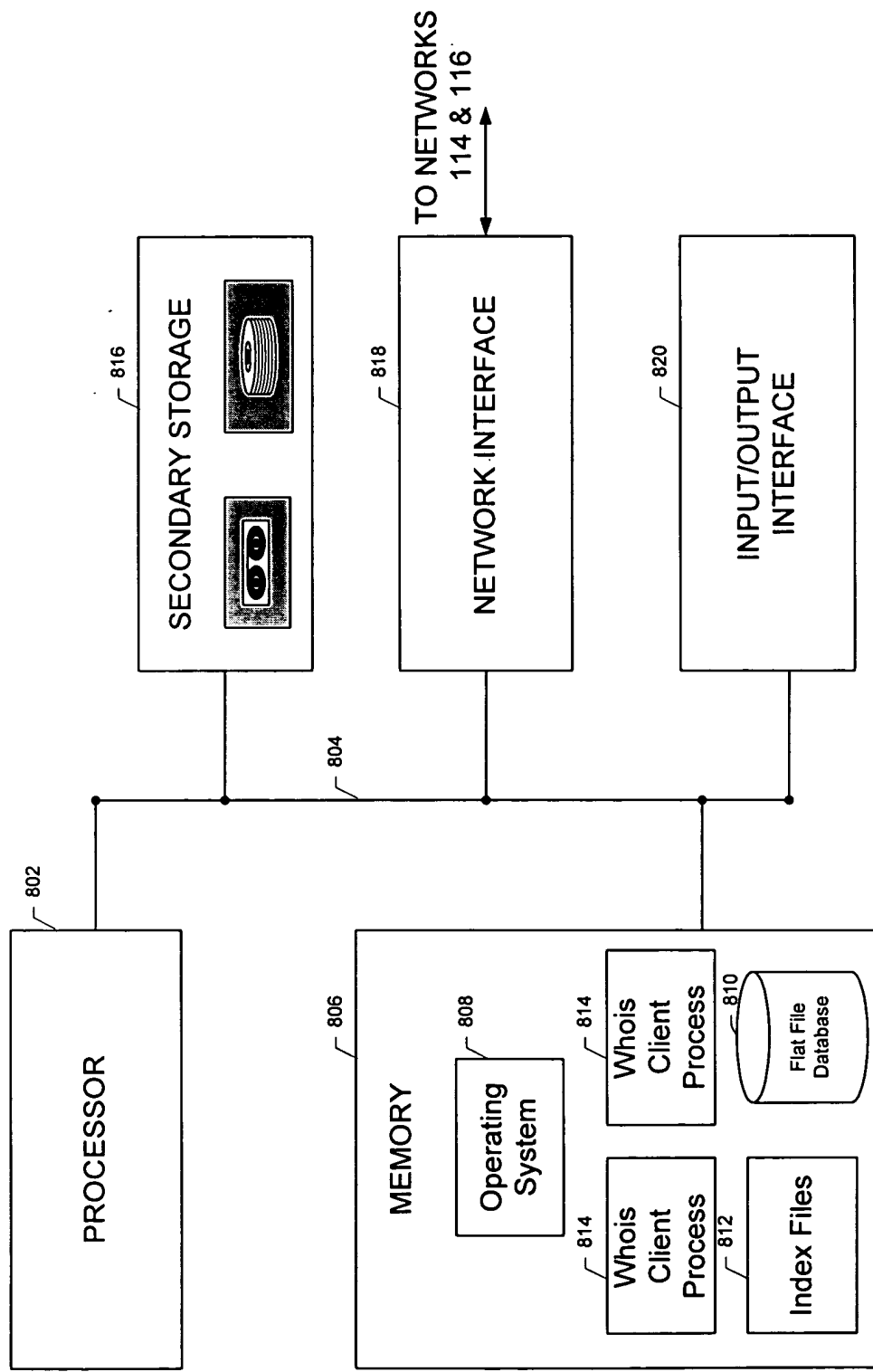


Figure 7

FIG. 8 is a block diagram of a system 800. The system 800 includes a processor 802, a memory 806, a secondary storage 816, a network interface 818, and an input/output interface 820. The processor 802 is connected to the memory 806, the secondary storage 816, the network interface 818, and the input/output interface 820. The memory 806 includes an operating system 808, a Whois Client Process 814, a Whois Client Process 814, and Index Files 812. The Whois Client Process 814 is connected to a Flat File Database 810. The network interface 818 is connected to networks 114 & 116.



800

Figure 8